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(51) International classification	:H04N0005372000, H02M0001000000, H03F0003450000, H01L0029100000, H01L0029861000	(71)Name of Applicant: 1)Raghvendra Singh Address of Applicant: Assistant Professor, Electronics and Communication Department, Rama University, Kanpur-209217 Uttar Pradesh India
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⁽⁵⁷⁾ Abstract:

The present invention relates to the designing AGC-PID using CMOS technology. In this patent, the electronics design and the implementation of the drive mode control by AGC-PID, using two stage op-amp. In this work we have designed various blocks as diode less Rectifier, low pass filter, sub-tractor and PID controller.

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